

FIG. 2a

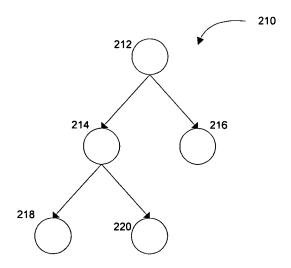
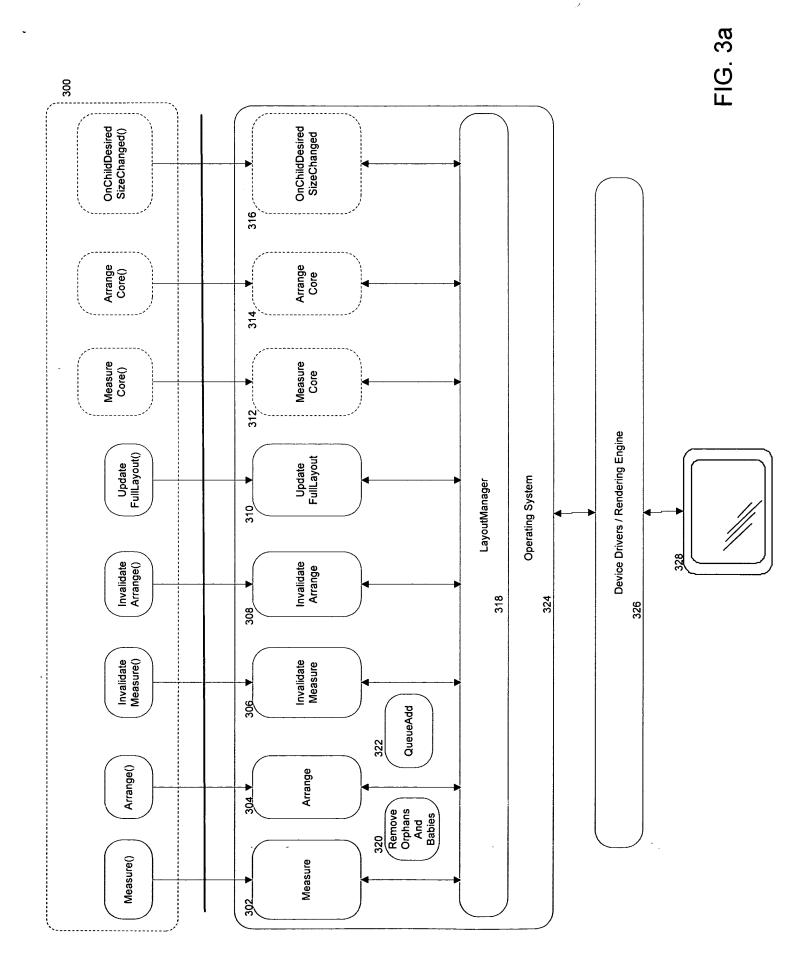


FIG. 2b



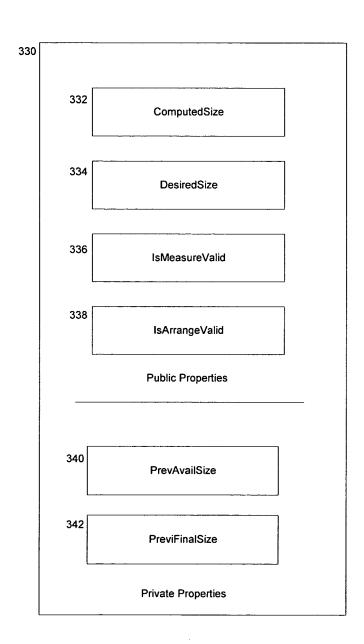


FIG. 3b

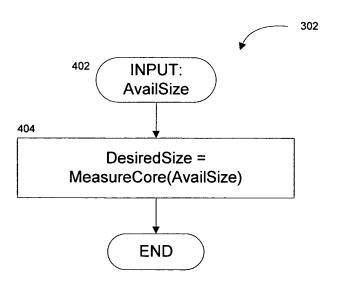


FIG. 4

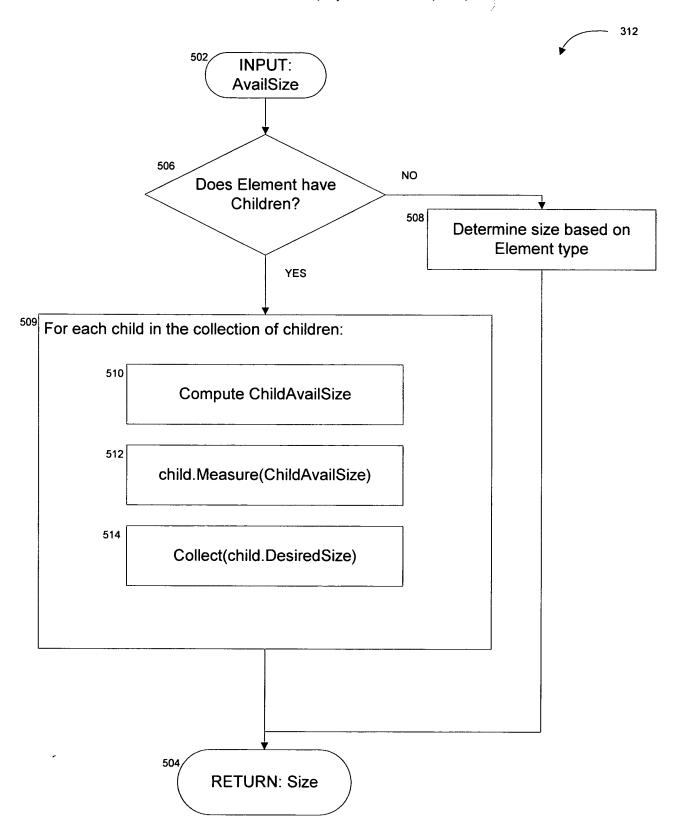


FIG. 5

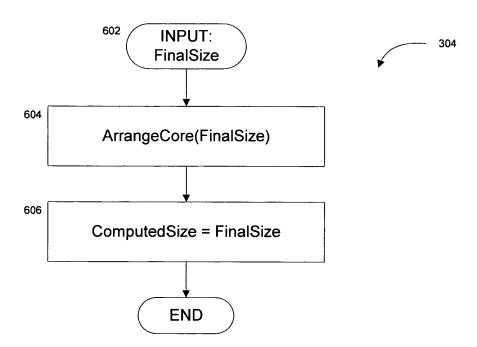


FIG. 6

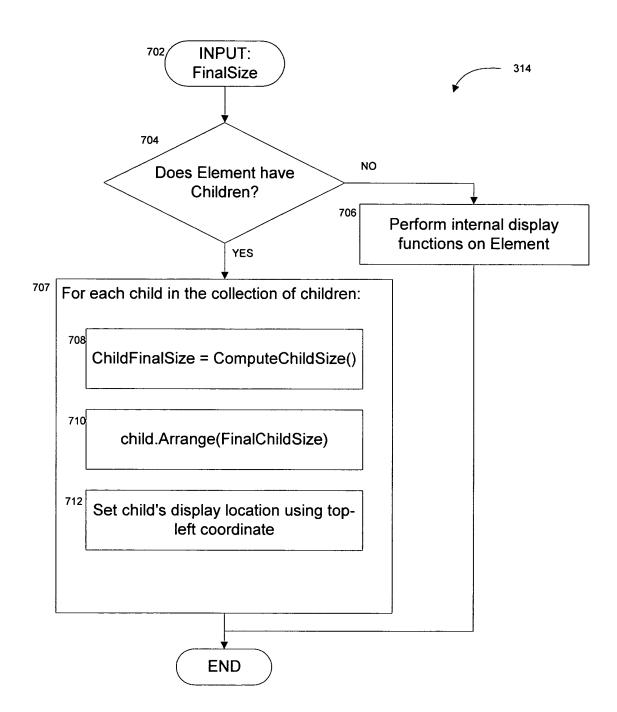


FIG. 7

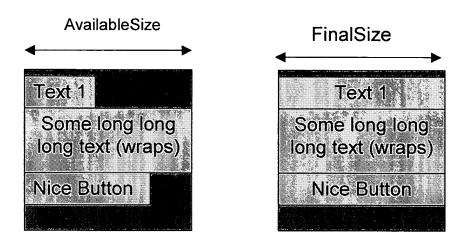


FIG. 8a

FIG. 8b

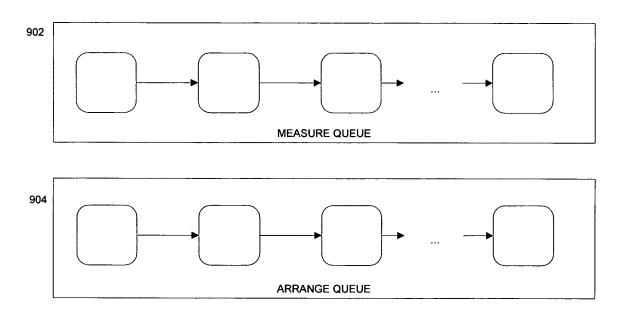
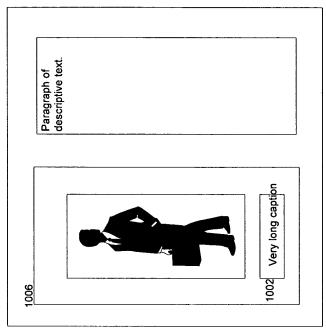
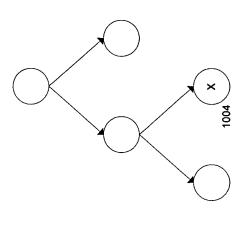
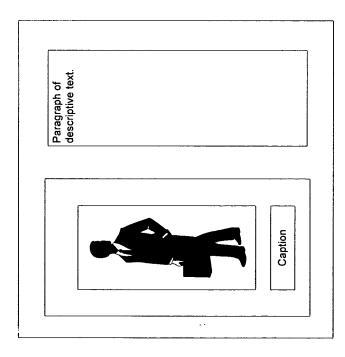


FIG. 9







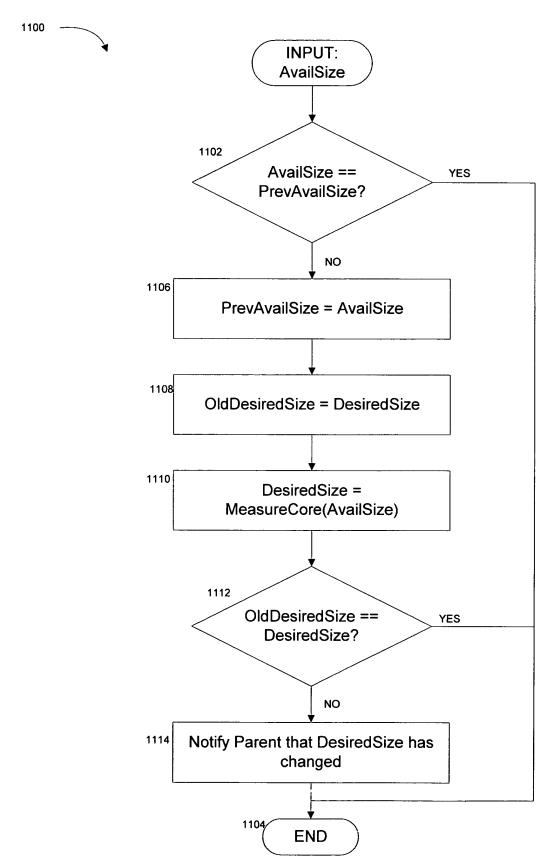


FIG. 11

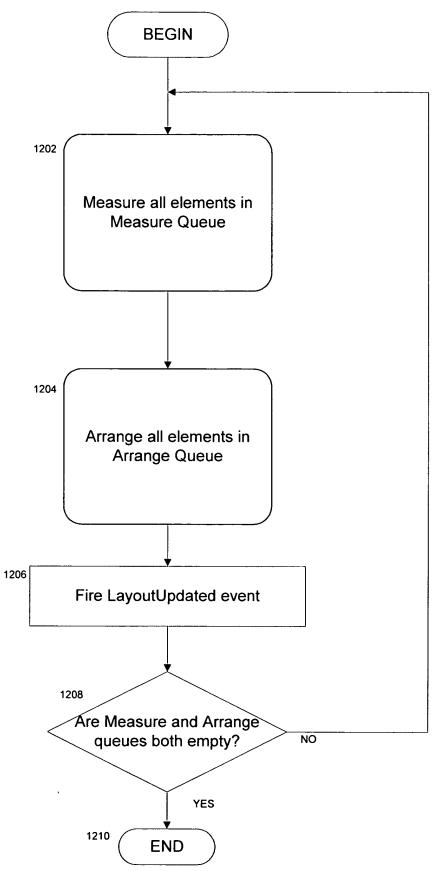


FIG. 12

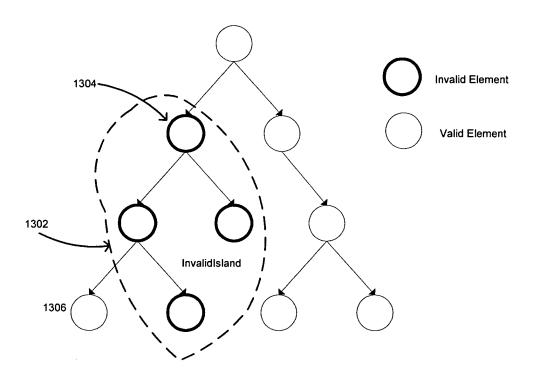
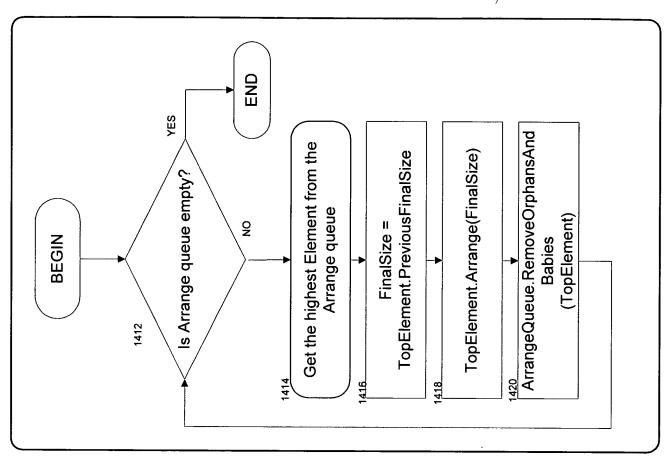


FIG. 13



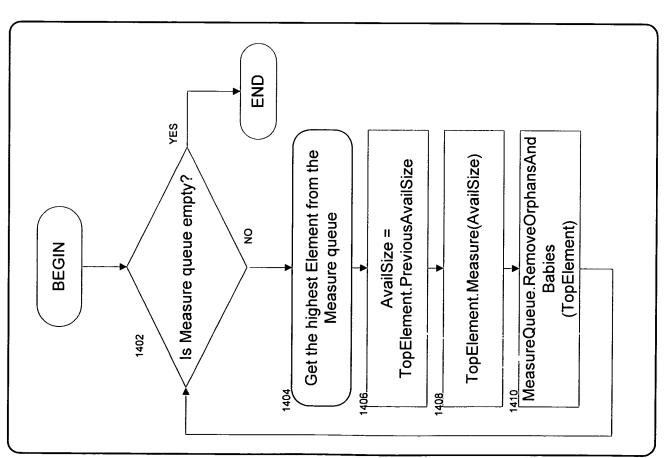
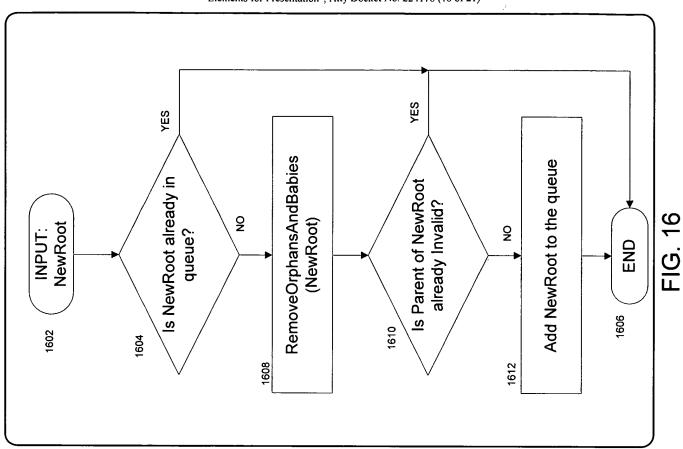
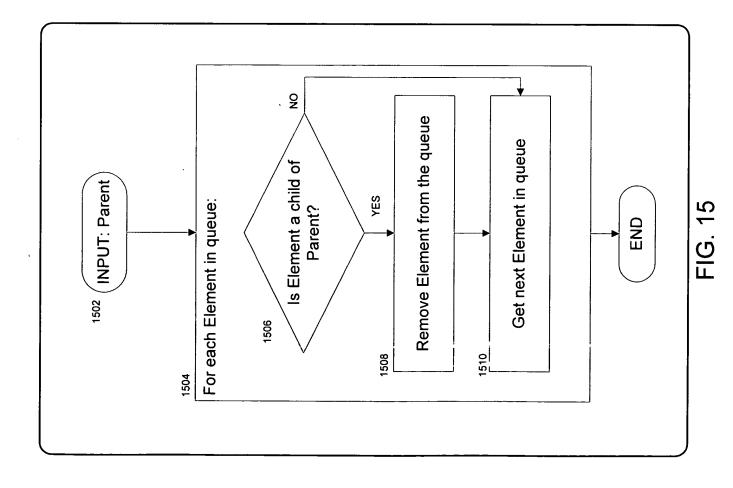


FIG. 14b

FIG. 14a





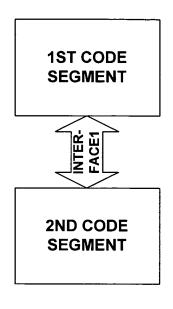


FIGURE A1

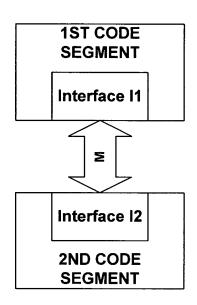


FIGURE A2

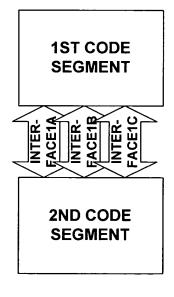


FIGURE B1

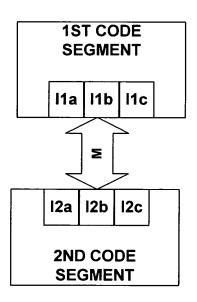
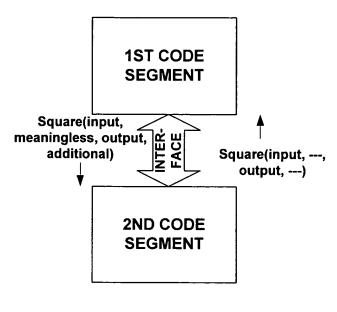


FIGURE B2



Interface I1'

Interface I2'

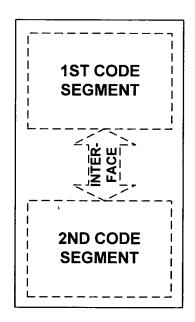
2ND CODE
SEGMENT

1ST CODE

FIGURE C1

FIGURE C2

1ST CODE



Interface I1"

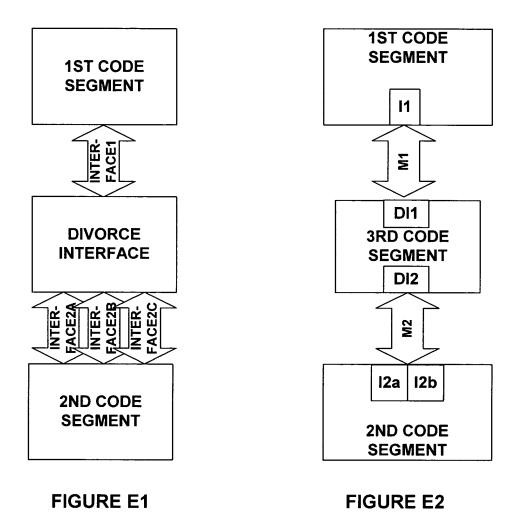
Interface I2A

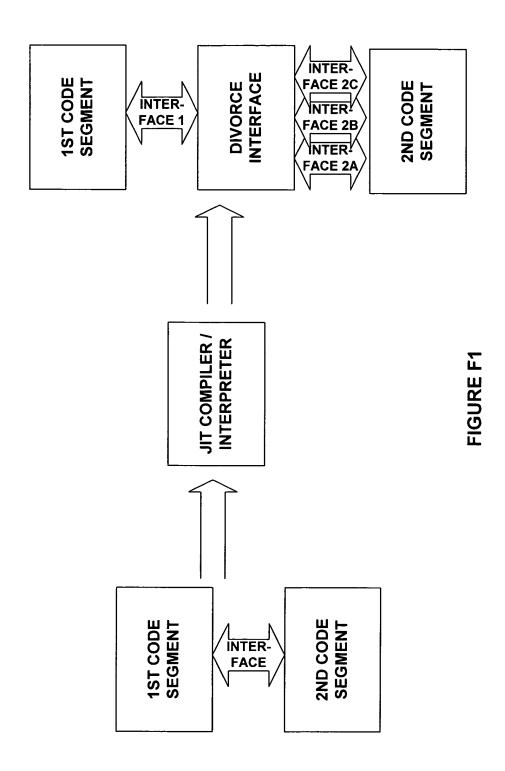
Interface I2B

2ND CODE
SEGMENT

FIGURE D1

FIGURE D2





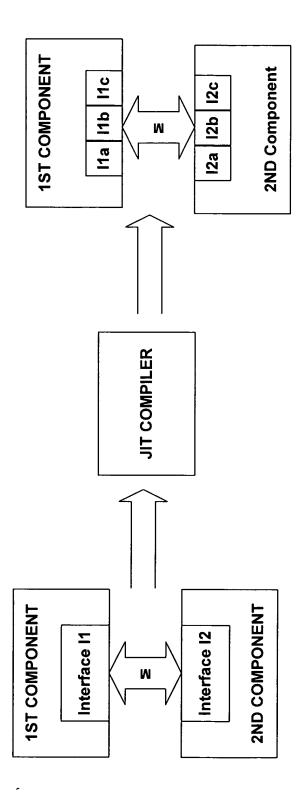


FIGURE FX